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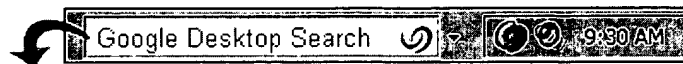
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Since the coded_block_pattern in NON-INTRA macroblocks **signals** every possible

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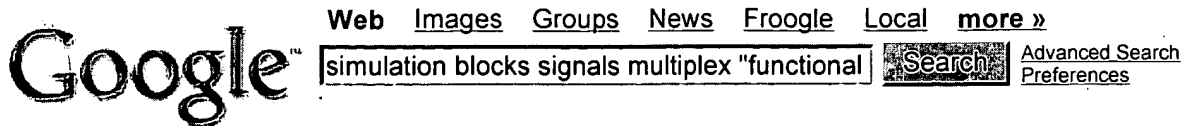
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commands. Figure 1.1 Principal **functional blocks** of a switching power converter

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preset **signals** to the WCD and RCD **blocks**, thus ensuring a >10-bit ... Initially,

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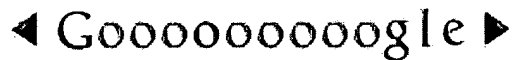
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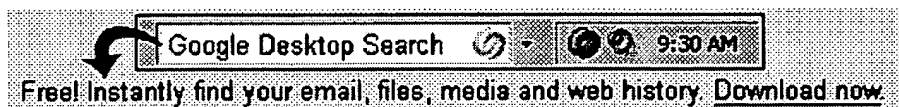
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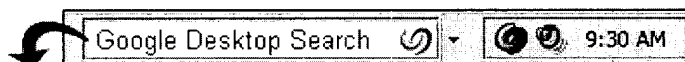
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Patent Assignment Abstract of Title

Total Assignments: 1**Application #:** 09870280 **Filing Dt:** 05/30/2001**Patent #:** NONE**Issue Dt:****PCT #:** NONE**Publication #:** US20020183998**Pub Dt:** 12/05/2002**Inventors:** Mojdeh Shakeri, Marc Ullman, Ramamurthy Mani**Title:** Block diagram modeling**Assignment: 1****Reel/Frame:** 011863/0939**Received:**
06/08/2001**Recorded:**
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5**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).**Assignors:** SHAKERI, MOJDEH**Exec Dt:** 05/22/2001ULLMAN, MARC**Exec Dt:** 05/24/2001MANI, RAMAMURTHY**Exec Dt:** 05/22/2001**Assignee:** MATHWORKS, INC., THE

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